

10/695 192

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	0	("L2" and "L4") and (system adj on adj chip)	USPAT	OR	OFF	2005/07/19 16:13
L6	0	("L2" and "L4") and (SDRAM) and (SOC)	USPAT	OR	OFF	2005/07/19 16:14
L7	1	("L2" and "L4") and (SDRAM) and (SOC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 16:14
L8	21	(semiconductor with package) and (SDRAM) and (SOC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 16:41
L9	21	(semiconductor with package) and (SDRAM) and (SOC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 16:42
L10	212	semiconductor\$3 and (SDRAM) and (SOC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 17:28
L11	255	((integrated adj circuit) or IC) and (SDRAM) and (SOC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 17:39

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S29	29386	((chip or die) with controller\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:07
S30	21134	((chip or die) with buffer\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:07
S32	6782	S29 and channel\$3	USPAT	OR	OFF	2005/07/19 13:08
S33	1649	S30 and S32	USPAT	OR	OFF	2005/07/19 13:09
S34	735	(S30 and S32) and package\$3	USPAT	OR	OFF	2005/07/19 13:09